

SCOPE OF CLAIM

1. A semiconductor device comprising a first transistor, a second transistor, and a switch, said semiconductor device characterized in that:

5 the first transistor comprises a gate terminal, a first terminal, and a second terminal;

 the second transistor comprises a gate terminal, a first terminal, and a second terminal;

 the gate terminal of the first transistor and the first terminal of the first
10 transistor are connected via the switch;

 the second terminal of the first transistor is connected to the first terminal of the second transistor;

 the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

15 a means for short-circuiting between the first terminal of the first transistor and the second terminal of the first transistor is provided.

2. A semiconductor device comprising a first transistor, a second transistor, a first switch, and a second switch, said semiconductor device characterized in that:

20 the first transistor comprises a gate terminal, a first terminal, and a second terminal;

 the second transistor comprises a gate terminal, a first terminal, and a second terminal;

 the gate terminal of the first transistor and the first terminal of the first
25 transistor are connected via the first switch;

 the second terminal of the first transistor is connected to the first terminal of the second transistor;

 the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

30 the first terminal of the first transistor and the second terminal of the first

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transistor are connected via the second switch.

3. The semiconductor device according to claim 1 or 2,

wherein the second terminal of the first transistor is connected to the first
5 terminal of the second transistor via a third transistor.

4. The semiconductor device according to claim 1 or 2, comprising a means
for short-circuiting between the first terminal of the second transistor and the second
terminal of the second transistor.

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5. A semiconductor device comprising a first transistor, a second transistor,
and a switch, said semiconductor device characterized in that:

the first transistor comprises a gate terminal, a first terminal, and a second
terminal;

15 the second transistor comprises a gate terminal, a first terminal, and a
second terminal;

the gate terminal of the first transistor and the first terminal of the first
transistor are connected via the switch;

the second terminal of the first transistor is connected to the first terminal of
20 the second transistor;

the gate terminal of the first transistor is connected to the gate terminal of
the second transistor; and

a means for short-circuiting between the first terminal of the second
transistor and the second terminal of the second transistor is provided.

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6. A semiconductor device comprising a first transistor, a second transistor,
a first switch, and a second switch, said semiconductor device characterized in that:

the first transistor comprises a gate terminal, a first terminal, and a second
terminal;

30 the second transistor comprises a gate terminal, a first terminal, and a

second terminal;

the gate terminal of the first transistor and the first terminal of the first transistor are connected via the first switch;

the second terminal of the first transistor is connected to the first terminal of
5 the second transistor;

the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

the first terminal of the second transistor and the second terminal of the second transistor are connected via the second switch.

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7. The semiconductor device according to claim 5 or 6,

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor via a third transistor.

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8. A semiconductor device comprising a first transistor, a second transistor, a first switch, a second switch, a third switch, and a wiring, said semiconductor device characterized in that:

the first transistor comprises a gate terminal, a first terminal, and a second terminal;

20 the second transistor comprises a gate terminal, a first terminal, and a second terminal;

the gate terminal of the first transistor and the first terminal of the first transistor are connected via the first switch;

the second terminal of the first transistor is connected to the first terminal of
25 the second transistor;

the gate terminal of the first transistor is connected to the gate terminal of the second transistor via a second switch; and

the gate terminal of the second transistor is connected to the wiring via a third switch.

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9. A semiconductor device comprising a first transistor, a second transistor, and a switch, said semiconductor device characterized in that:

the first transistor comprises a gate terminal, a first terminal, and a second terminal;

5 the second transistor comprises a gate terminal, a first terminal, and a second terminal;

the gate terminal of the first transistor and the first terminal of the first transistor are connected via the switch;

the second terminal of the first transistor is connected to the first terminal of
10 the second transistor;

the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

a means for short-circuiting at least either of between the first terminal of the first transistor and the second terminal of the first transistor or between the first
15 terminal of the second transistor and the second terminal of the second transistor is provided.

10. A semiconductor device comprising a first transistor, a second transistor, a first switch, and a second switch, said semiconductor device characterized in that:

20 the first transistor comprises a gate terminal, a first terminal, and a second terminal;

the second transistor comprises a gate terminal, a first terminal, and a second terminal;

the gate terminal of the first transistor and the first terminal of the first
25 transistor are connected via the first switch;

the second terminal of the first transistor is connected to the first terminal of the second transistor;

the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

30 the second switch is disposed at least either of between the first terminal of

the first transistor and the second terminal of the first transistor or between the first terminal of the second transistor and the second terminal of the second transistor.

11. The semiconductor device according to any one of claims 1, 2, 5, 6, 8, 9,
5 or 10,

wherein the first transistor and the second transistor have the same conductivity.

12. The semiconductor device according to any one of claims 1, 2, 5, 6, 8, 9,
10 or 10,

wherein a capacitor is provided and the gate terminal of the first transistor and one terminal of the capacitor are connected.

13. The semiconductor device according to claim 12,
15 wherein the gate terminal of the first transistor is connected to one terminal of the capacitor, and the other terminal of the capacitor is connected to a second terminal of the second transistor.

14. The semiconductor device according to any one of claims 1, 2, 5, 6, 8, 9,
20 or 10,

wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a current source circuit.

15. The semiconductor device according to any one of claims 1, 2, 5, 6, 8, 9,
25 or 10,

wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a display element.

16. The semiconductor device according to claim 15,
30 wherein the display element is an EL element.

17. A display device comprising the semiconductor device according to any one of claims 1, 2, 5, 6, 9, 10, or 11.

5 18. An electronic apparatus comprising the display device according to claim 17.

19. A digital still camera comprising the semiconductor device according to any one of claims 1, 2, 5, 6, 9, 10 or 11.

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20. A personal computer comprising the semiconductor device according to any one of claims 1, 2, 5, 6, 9, 10 or 11.

21. A video camera comprising the semiconductor device according to any one of claims 1, 2, 5, 6, 9, 10 or 11.

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22. A portable phone comprising the semiconductor device according to any one of claims 1, 2, 5, 6, 9, 10 or 11.

20 23. An image reproducing apparatus provided with a recording medium, comprising the semiconductor device according to any one of claims 1, 2, 5, 6, 9, 10 or 11.